

3-971 U.S. PTO
10/001625
10/24/01

U.S. UTILITY Patent Application

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PATENT NUMBER and
ISSUE DATE

APPL NUM 10001625	FILING DATE 10/24/2001	CLASS 2811	SUBCLASS	GAU 2811	EXAMINER
<p>**APPLICANTS: Pie Federico.</p>					
<p>**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/405,506 09/23/1999</p>					
<p>** FOREIGN APPLICATIONS VERIFIED: EUROPEAN PATENT OFFICE (EPO) 98830562 9 09/25/1998</p>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed		35 USC 112 conditions met		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners initials				854963 51ED1	
<p>TITLE : Method of manufacturing an integrated semiconductor device having a plurality of connected levels</p>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drawg.	Figs. Drawg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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